

# 5SND 0500N330300

## HiPak IGBT Module

$$V_{CE} = 3300 \text{ V}$$

$$I_C = 500 \text{ A}$$

Ultra low-loss, rugged SPT+ chip-set  
 Smooth switching SPT+ chip-set for good EMC  
 AlSiC base-plate for high power cycling capability  
 AlN substrate for low thermal resistance  
 2 switches in one package



### Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	max	Unit
Collector-emitter voltage	$V_{CES}$	$V_{GE} = 0 \text{ V}$ , $T_{vj} \geq 25 \text{ }^\circ\text{C}$		3300	V
DC collector current	$I_C$	$T_C = 100 \text{ }^\circ\text{C}$ , $T_{vj} = 150 \text{ }^\circ\text{C}$		500	A
Peak collector current	$I_{CM}$	$t_p = 1 \text{ ms}$		1000	A
Gate-emitter voltage	$V_{GES}$		-20	20	V
Total power dissipation	$P_{tot}$	$T_C = 25 \text{ }^\circ\text{C}$ , $T_{vj} = 150 \text{ }^\circ\text{C}$ , per switch		4800	W
DC forward current	$I_F$			500	A
Peak forward current	$I_{FRM}$	$t_p = 1 \text{ ms}$		1000	A
Surge current	$I_{FSM}$	$V_R = 0 \text{ V}$ , $T_{vj} = 150 \text{ }^\circ\text{C}$ , $t_p = 10 \text{ ms}$ , half-sinewave		4500	A
IGBT short circuit SOA	$t_{psc}$	$V_{CC} = 2500 \text{ V}$ , $V_{CEM \text{ CHIP}} \leq 3300 \text{ V}$ $V_{GE} \leq 15 \text{ V}$ , $T_{vj} \leq 150 \text{ }^\circ\text{C}$		10	$\mu\text{s}$
Isolation voltage	$V_{isol}$	1 min, $f = 50 \text{ Hz}$		6000	V
Junction temperature	$T_{vj}$			175	$^\circ\text{C}$
Junction operating temperature	$T_{vj(op)}$		-50	150	$^\circ\text{C}$
Case temperature	$T_C$		-50	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$		-50	125	$^\circ\text{C}$
Mounting torques <sup>2)</sup>	$M_s$	Base-heatsink, M6 screws	4	6	Nm
	$M_{t1}$	Main terminals, M8 screws	8	10	
	$M_{t2}$	Auxiliary terminals, M4 screws	2	3	

<sup>1)</sup> Maximum rated values indicate limits beyond which damage to the device may occur per IEC 60747

<sup>2)</sup> for detailed mounting instructions refer to ABB Document No. 5SYA 2039

### IGBT characteristic values <sup>3)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Collector (-emitter) breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0 \text{ V}$ , $I_C = 10 \text{ mA}$ , $T_{vj} = 25 \text{ °C}$	3300			V
Collector-emitter <sup>4)</sup> saturation voltage	$V_{CE \text{ sat}}$	$I_C = 500 \text{ A}$ , $V_{GE} = 15 \text{ V}$	$T_{vj} = 25 \text{ °C}$	2.5	2.9	V
			$T_{vj} = 125 \text{ °C}$	3.1		V
			$T_{vj} = 150 \text{ °C}$	3.25	3.4	V
Collector cut-off current	$I_{CES}$	$V_{CE} = 3300 \text{ V}$ , $V_{GE} = 0 \text{ V}$	$T_{vj} = 25 \text{ °C}$		0.3	mA
			$T_{vj} = 125 \text{ °C}$	6.5	13	mA
			$T_{vj} = 150 \text{ °C}$	30		mA
Gate leakage current	$I_{GES}$	$V_{CE} = 0 \text{ V}$ , $V_{GE} = \pm 20 \text{ V}$ , $T_{vj} = 125 \text{ °C}$	-500		500	nA
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 80 \text{ mA}$ , $V_{CE} = V_{GE}$ , $T_{vj} = 25 \text{ °C}$	5		7	V
Gate charge	$Q_G$	$I_C = 500 \text{ A}$ , $V_{CE} = 1800 \text{ V}$ , $V_{GE} = -15 \text{ V} \dots 15 \text{ V}$		3.7		$\mu\text{C}$
Input capacitance	$C_{ies}$	$V_{CE} = 25 \text{ V}$ , $V_{GE} = 0 \text{ V}$ , $f = 1 \text{ MHz}$ , $T_{vj} = 25 \text{ °C}$		50		nF
Output capacitance	$C_{oes}$			4.2		nF
Reverse transfer capacitance	$C_{res}$			1.28		nF
Internal gate resistance	$R_{Gint}$	per switch		1.8		$\Omega$
Turn-on delay time	$t_{d(on)}$	$V_{CC} = 1800 \text{ V}$ , $I_C = 500 \text{ A}$ , $R_G = 3.9 \text{ }\Omega$ , $V_{GE} = \pm 15 \text{ V}$ , $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ °C}$	380		ns
			$T_{vj} = 125 \text{ °C}$	400		ns
			$T_{vj} = 150 \text{ °C}$	400		ns
Rise time	$t_r$	$V_{CC} = 1800 \text{ V}$ , $I_C = 500 \text{ A}$ , $R_G = 3.9 \text{ }\Omega$ , $V_{GE} = \pm 15 \text{ V}$ , $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ °C}$	135		ns
			$T_{vj} = 125 \text{ °C}$	140		ns
			$T_{vj} = 150 \text{ °C}$	145		ns
Turn-off delay time	$t_{d(off)}$	$V_{CC} = 1800 \text{ V}$ , $I_C = 500 \text{ A}$ , $R_G = 3.9 \text{ }\Omega$ , $V_{GE} = \pm 15 \text{ V}$ , $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ °C}$	1250		ns
			$T_{vj} = 125 \text{ °C}$	1450		ns
			$T_{vj} = 150 \text{ °C}$	1460		ns
Fall time	$t_f$	$V_{CC} = 1800 \text{ V}$ , $I_C = 500 \text{ A}$ , $R_G = 3.9 \text{ }\Omega$ , $V_{GE} = \pm 15 \text{ V}$ , $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ °C}$	255		ns
			$T_{vj} = 125 \text{ °C}$	340		ns
			$T_{vj} = 150 \text{ °C}$	360		ns
Turn-on switching energy	$E_{on}$	$V_{CC} = 1800 \text{ V}$ , $I_C = 500 \text{ A}$ , $R_G = 3.9 \text{ }\Omega$ , $V_{GE} = \pm 15 \text{ V}$ , $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ °C}$	660		mJ
			$T_{vj} = 125 \text{ °C}$	860		mJ
			$T_{vj} = 150 \text{ °C}$	960		mJ
Turn-off switching energy	$E_{off}$	$V_{CC} = 1800 \text{ V}$ , $I_C = 500 \text{ A}$ , $R_G = 3.9 \text{ }\Omega$ , $V_{GE} = \pm 15 \text{ V}$ , $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ °C}$	620		mJ
			$T_{vj} = 125 \text{ °C}$	850		mJ
			$T_{vj} = 150 \text{ °C}$	920		mJ
Short circuit current	$I_{SC}$	$t_{psc} \leq 10 \text{ }\mu\text{s}$ , $V_{GE} = 15 \text{ V}$ , $V_{CC} = 2500 \text{ V}$ , $V_{CEM \text{ CHIP}} \leq 3300 \text{ V}$		2170		A

<sup>3)</sup> Characteristic values according to IEC 60747 - 9

<sup>4)</sup> Collector-emitter saturation voltage is given at chip level

## Diode characteristic values <sup>5)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Forward voltage <sup>6)</sup>	$V_F$	$I_F = 500 \text{ A}$	$T_{vj} = 25 \text{ }^\circ\text{C}$	2.05		V
			$T_{vj} = 125 \text{ }^\circ\text{C}$	2.25		V
			$T_{vj} = 150 \text{ }^\circ\text{C}$	2.20		V
Peak reverse recovery current	$I_{RM}$		$T_{vj} = 25 \text{ }^\circ\text{C}$	690		A
			$T_{vj} = 125 \text{ }^\circ\text{C}$	810		A
			$T_{vj} = 150 \text{ }^\circ\text{C}$	830		A
Recovered charge	$Q_r$	$V_{CC} = 1800 \text{ V}$ , $I_F = 500 \text{ A}$ , $V_{GE} = \pm 15 \text{ V}$ , $R_G = 3.9 \text{ }^\circ\Omega$ , $di/dt = 3.8 \text{ kA}/\mu\text{s}$ $L_\sigma = 125 \text{ nH}$ , inductive load	$T_{vj} = 25 \text{ }^\circ\text{C}$	360		$\mu\text{C}$
			$T_{vj} = 125 \text{ }^\circ\text{C}$	585		$\mu\text{C}$
			$T_{vj} = 150 \text{ }^\circ\text{C}$	680		$\mu\text{C}$
Reverse recovery time	$t_{rr}$		$T_{vj} = 25 \text{ }^\circ\text{C}$	930		ns
			$T_{vj} = 125 \text{ }^\circ\text{C}$	1250		ns
			$T_{vj} = 150 \text{ }^\circ\text{C}$	1410		ns
Reverse recovery energy	$E_{rec}$		$T_{vj} = 25 \text{ }^\circ\text{C}$	400		mJ
			$T_{vj} = 125 \text{ }^\circ\text{C}$	680		mJ
			$T_{vj} = 150 \text{ }^\circ\text{C}$	800		mJ

<sup>5)</sup> Characteristic values according to IEC 60747 - 2

<sup>6)</sup> Forward voltage is given at chip level

## Package properties <sup>7)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
IGBT thermal resistance junction to case	$R_{th(j-c)IGBT}$	per switch			0.026	K/W
Diode thermal resistance junction to case	$R_{th(j-c)DIODE}$				0.05	K/W
IGBT thermal resistance <sup>2)</sup> case to heatsink	$R_{th(c-s)IGBT}$	IGBT per switch, $\lambda$ grease = $1\text{W}/\text{m} \times \text{K}$		0.024		K/W
Diode thermal resistance <sup>2)</sup> case to heatsink	$R_{th(c-s)DIODE}$	Diode per switch, $\lambda$ grease = $1\text{W}/\text{m} \times \text{K}$		0.048		K/W
Partial discharge extinction voltage	$V_e$	$f = 50 \text{ Hz}$ , $Q_{PD} \leq 10 \text{ pC}$ (acc. To IEC 61287)	3500			V
Comparative tracking index	CTI		600			
Module stray inductance	$L_{\sigma CE}$	per switch		24		nH
Resistance, terminal-chip	$R_{CC'+EE'}$	per switch	$T_C = 25 \text{ }^\circ\text{C}$	0.180		m $\Omega$
			$T_C = 125 \text{ }^\circ\text{C}$	0.245		
			$T_C = 150 \text{ }^\circ\text{C}$	0.260		

<sup>2)</sup> For detailed mounting instructions refer to ABB Document No. 5SYA 2039

## Mechanical properties <sup>7)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Dimensions	L x W x H	Typical		130 x 160 x 38		mm
Clearance distance in air	$d_a$	according to IEC 60664-1 and EN 50124-1	Term. to base:	19		mm
			Term. to term:	19		
Surface creepage distance	$d_s$	according to IEC 60664-1 and EN 50124-1	Term. to base:	28.2		mm
			Term. to term:	28.2		
Mass	m			850		g

<sup>7)</sup> Package and mechanical properties according to IEC 60747 - 15



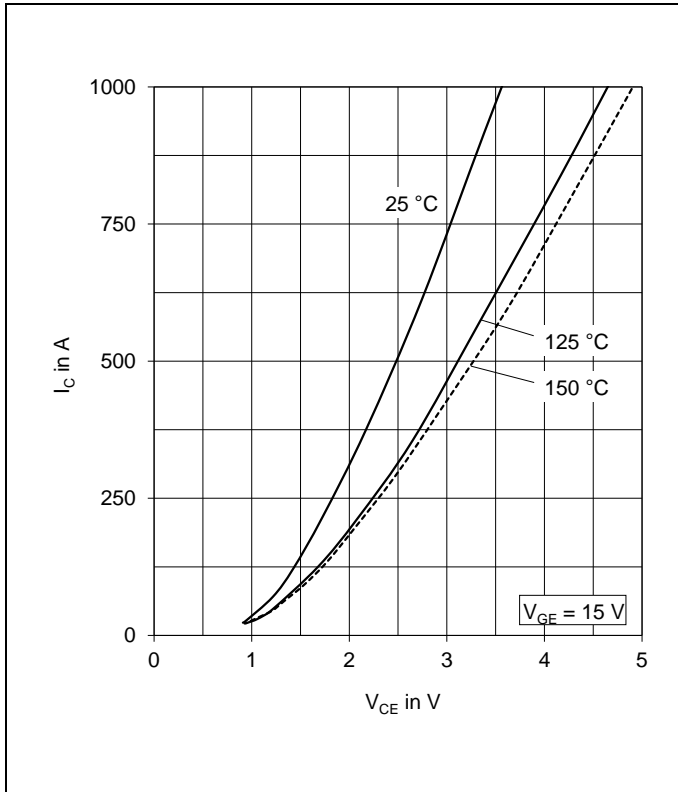


Fig. 1 Typical on-state characteristics, chip level

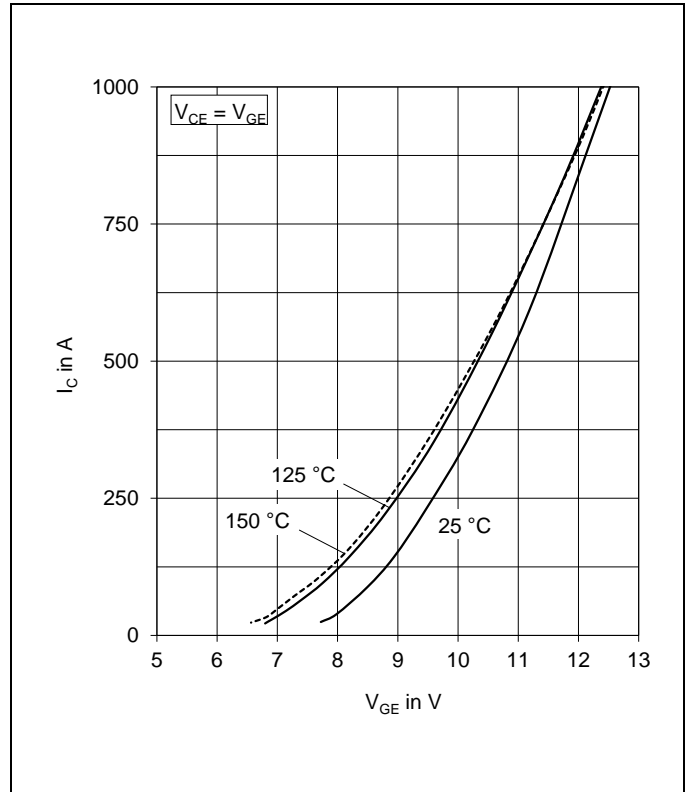


Fig. 2 Typical transfer characteristics, chip level

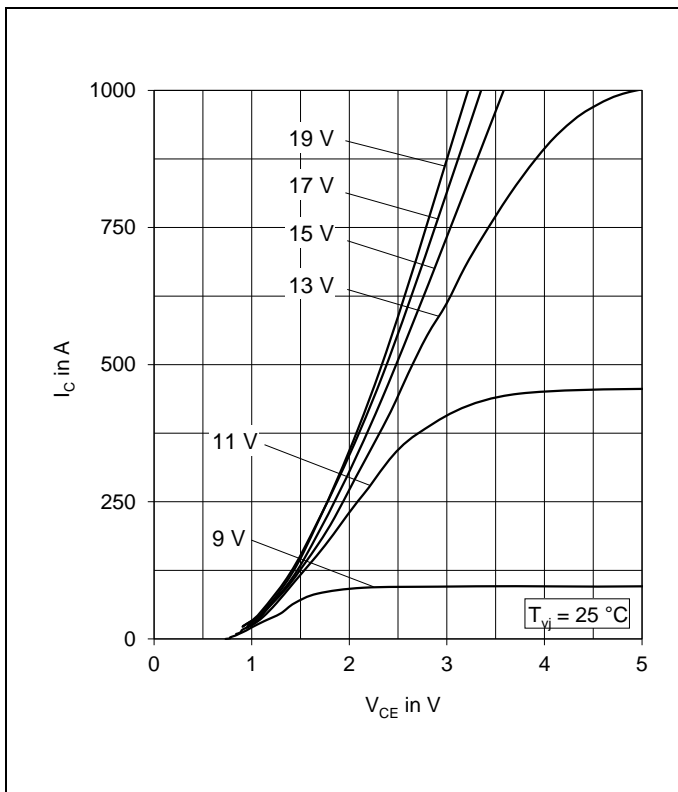


Fig. 3 Typical output characteristics, chip level

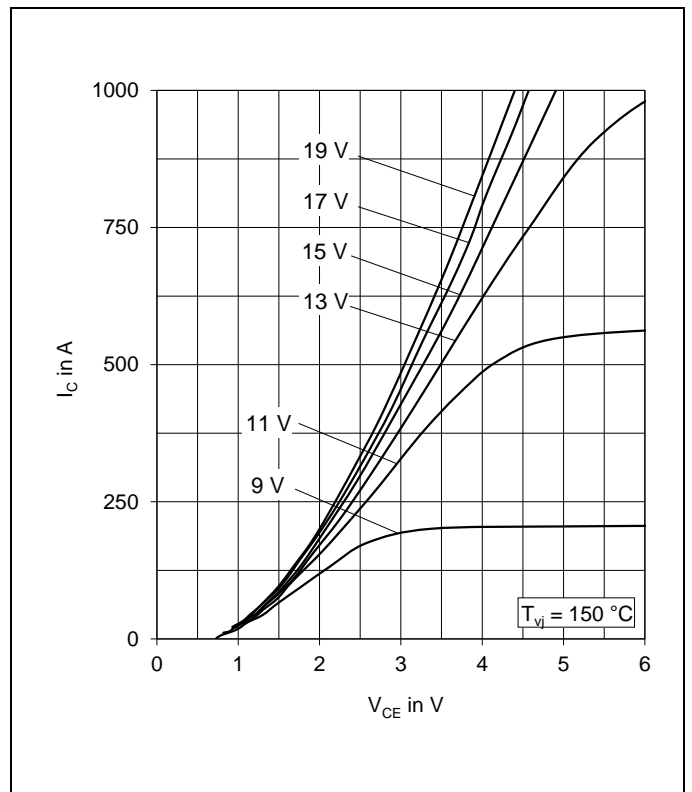


Fig. 4 Typical output characteristics, chip level

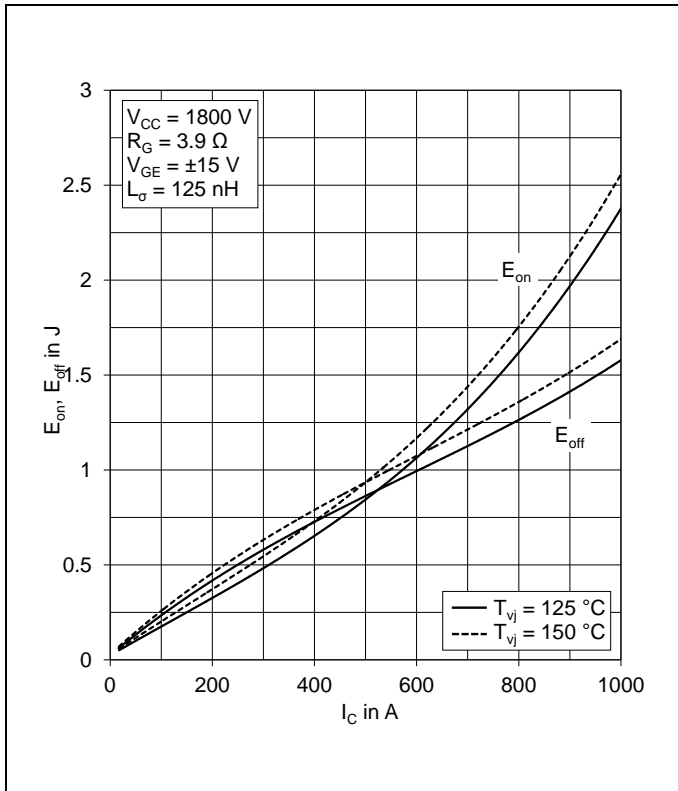


Fig. 5 Typical switching energies per pulse vs. collector current

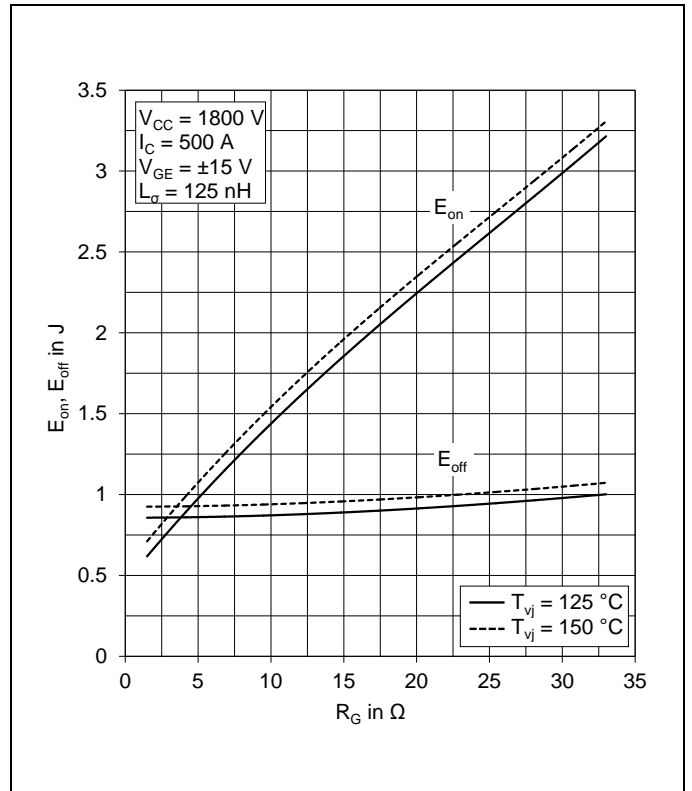


Fig. 6 Typical switching energies per pulse vs. gate resistor

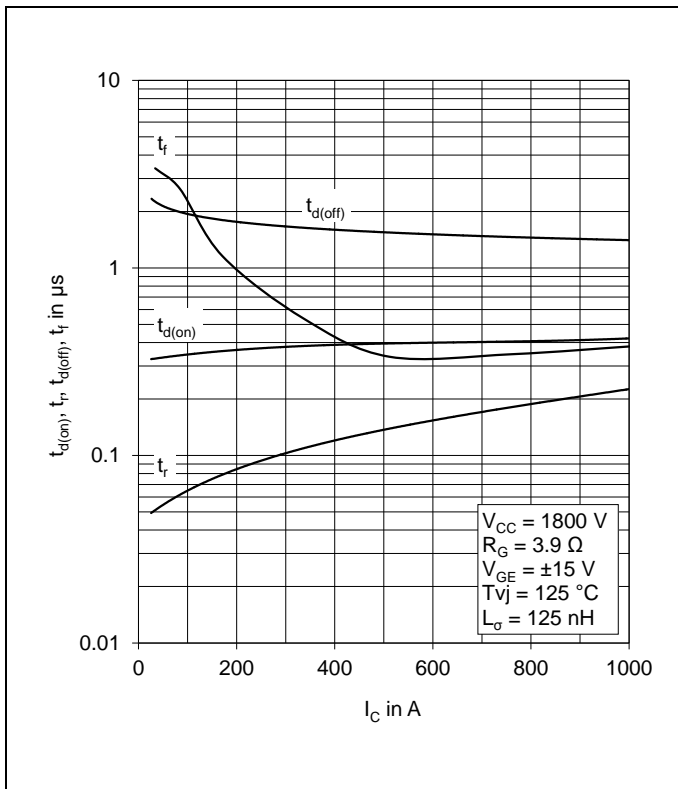


Fig. 7 Typical switching times vs. collector current

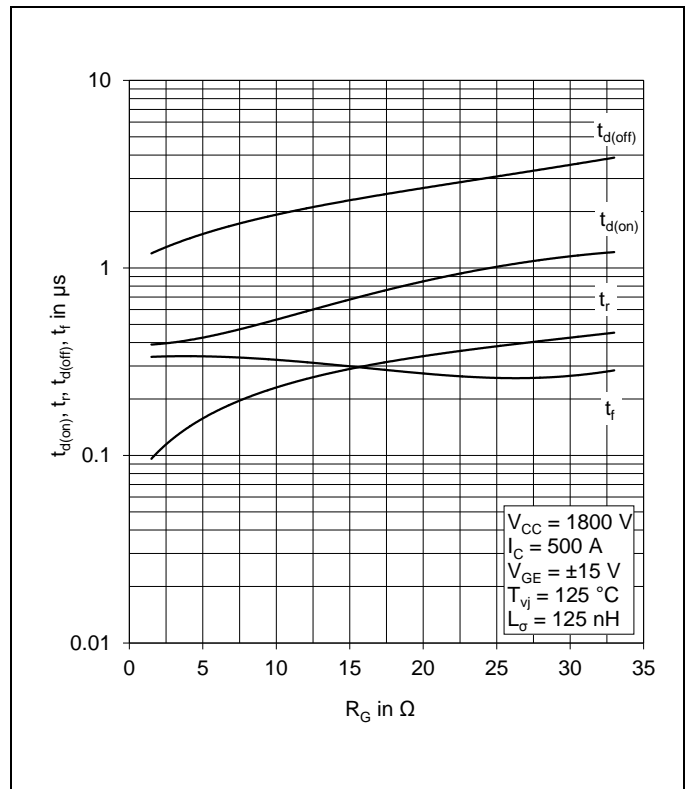


Fig. 8 Typical switching times vs. gate resistor

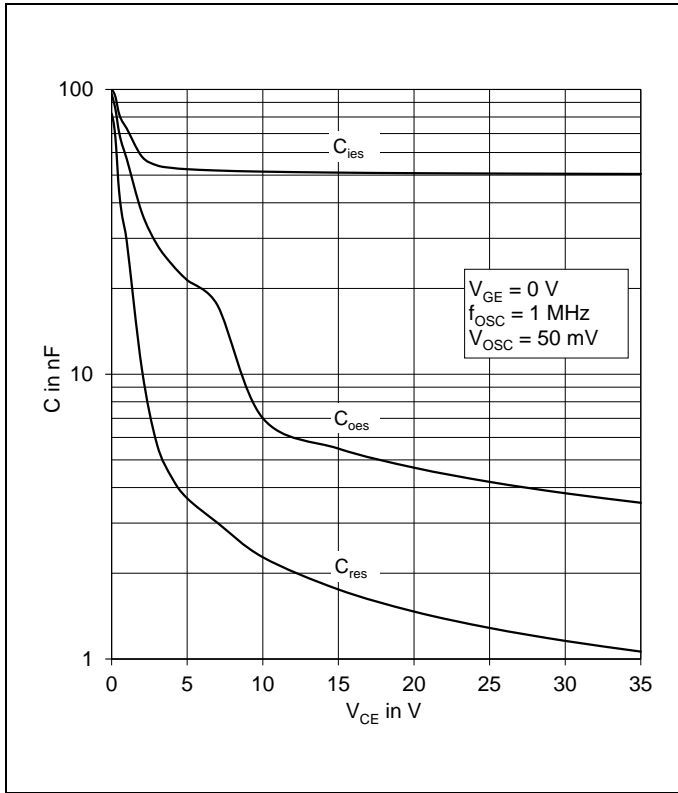


Fig. 9 Typical capacitances vs. collector-emitter voltage

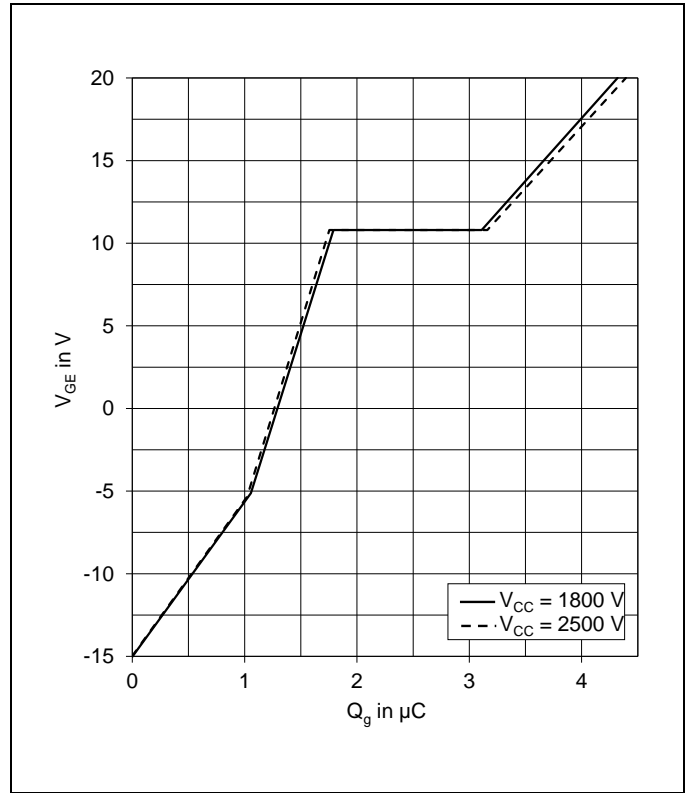


Fig. 10 Typical gate charge characteristics

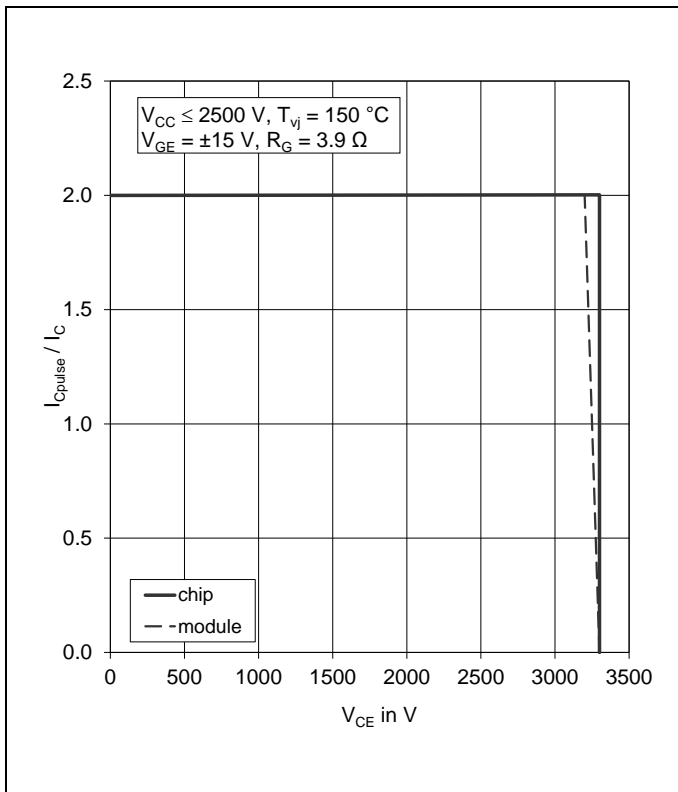


Fig. 11 Turn-off safe operating area (RBSOA)

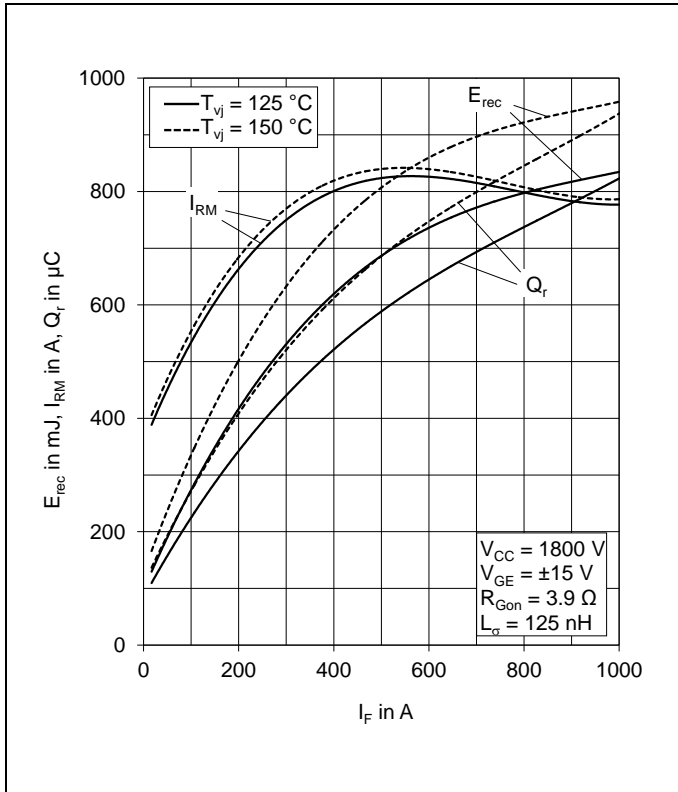


Fig. 12 Typical reverse recovery characteristics vs. forward current

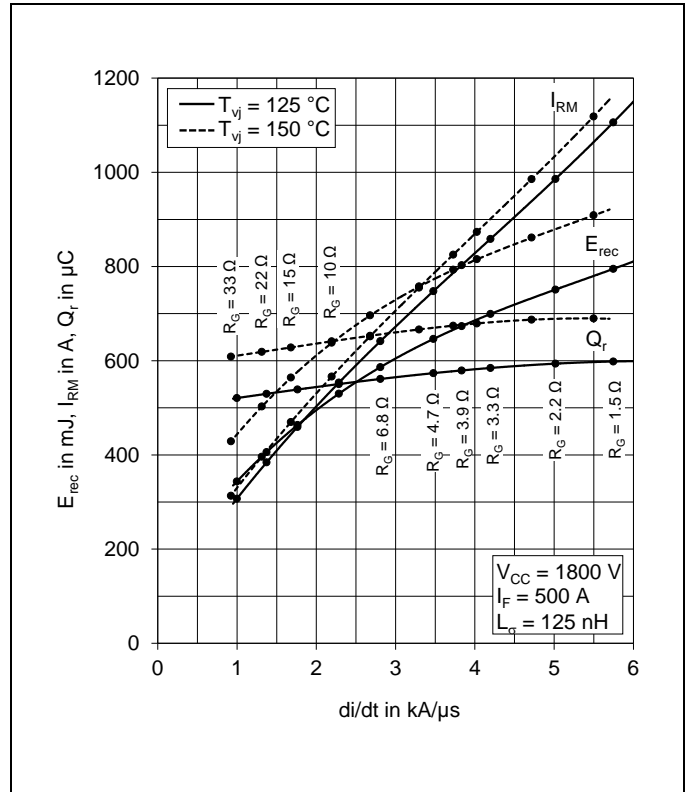


Fig. 13 Typical reverse recovery characteristics vs. di/dt

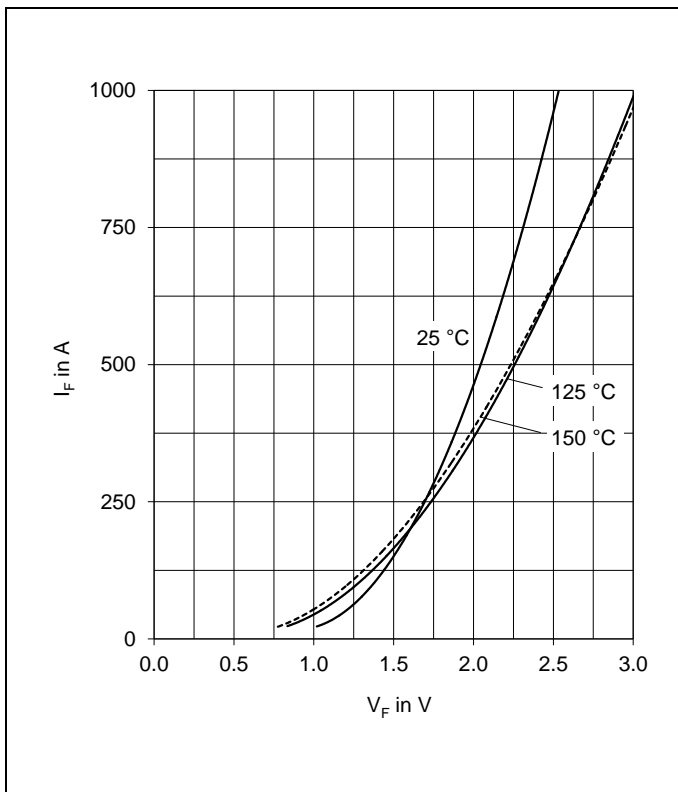


Fig. 14 Typical diode forward characteristics chip level

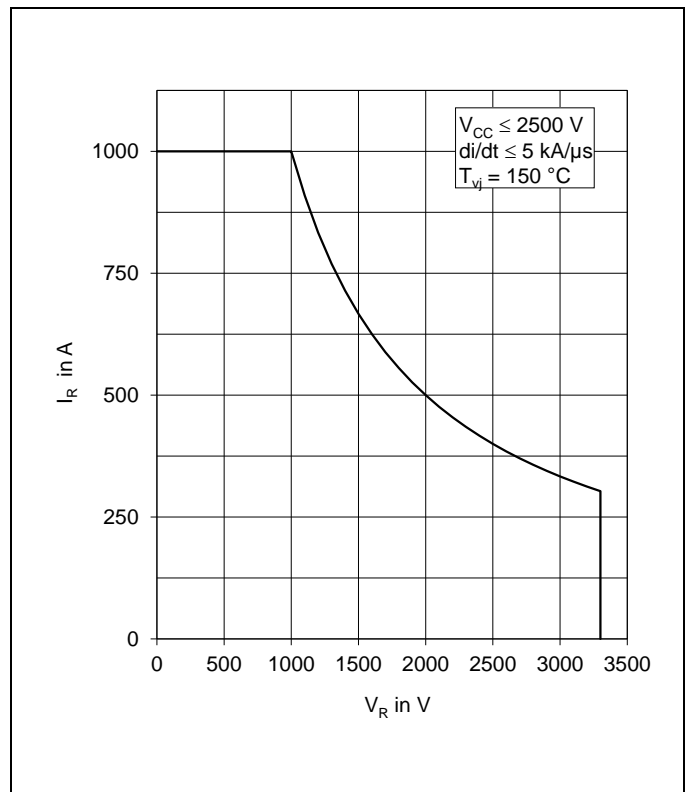


Fig. 15 Safe operating area diode (SOA)



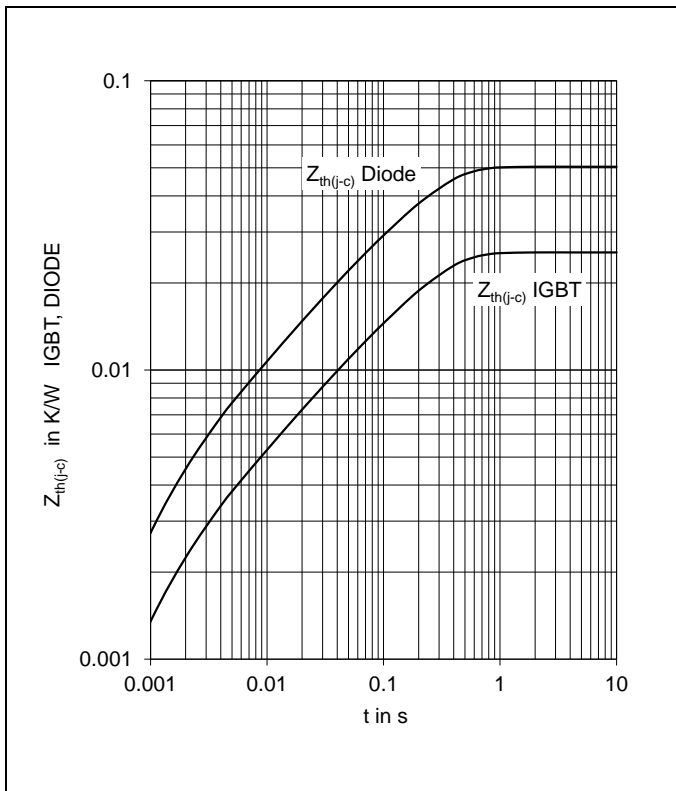


Fig. 16 Thermal impedance vs. time

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i (1 - e^{-t/\tau_i})$$

	i	1	2	3	4	5
IGBT	\$R_i\$ (K/kW)	17.56	4.12	1.922	1.896	
	\$\tau_i\$ (ms)	207.4	30.1	7.55	1.57	
DIODE	\$R_i\$ (K/kW)	34.2	8.56	3.84	3.84	
	\$\tau_i\$ (ms)	203.6	30.1	7.53	1.57	

**Related documents:**

- 5SYA 2042 Failure rates of HiPak modules due to cosmic rays
- 5SYA 2043 Load - cycle capability of HiPaks
- 5SYA 2045 Thermal runaway during blocking
- 5SYA 2053 Applying IGBT
- 5SYA 2058 Surge currents for IGBT diodes
- 5SYA 2093 Thermal design and temperature ratings of IGBT modules
- 5SYA 2098 Paralleling of IGBT modules
- 5SZK 9111 Specification of environmental class for HiPak Storage
- 5SZK 9112 Specification of environmental class for HiPak Transportation
- 5SZK 9113 Specification of environmental class for HiPak Operation (Industry)
- 5SZK 9120 Specification of environmental class for HiPak

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